

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

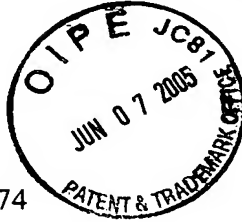
In re the Application of

Shinji HIRANO

Application No.: 10/762,474

Filed: January 23, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING FUSE DEVICE AND
METHOD FOR MANUFACTURING THE SAME



Group Art Unit: 2826

Examiner: F. ERDEM

Docket No.: 118267

AMENDMENT UNDER 37 C.F.R. §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the January 26, 2005, Office Action, the period for reply being extended by the attached Petition for Extension of Time, and further to the May 31, 2005, personal interview, please consider the following:

Amendments to the Claims as reflected in the listing of claims; and

Remarks.